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APPLICATION NUMBER	FILING or 371(c) DATE	GRP ART UNIT	FIL FEE REC'D	ATTY.DOCKET.NO	DRAWINGS	TOT CLAIMS	IND CLAIMS
10/552.948	09/11/2006	2827	1030	SNDK.284US1	3	19	3

CONFIRMATION NO. 6753

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FILING RECEIPT

Date Mailed: 12/01/2006

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Applicant(s)

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Assignment For Published Patent Application

SANDISK CORPORATION, Sunnyvale, CA

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Domestic Priority data as claimed by applicant

This application is a 371 of PCT/US04/10991 04/08/2004

Foreign Applications

UNITED STATES OF AMERICA 10/414.132 04/14/2003

If Required, Foreign Filing License Granted: 11/30/2006

The country code and number of your priority application, to be used for filing abroad under the Paris Convention, is US10/552.948

Projected Publication Date: 03/08/2007

Non-Publication Request: No Early Publication Request: No

Title

Read and erase verify methods and circuits suitable for low voltage non-volatile memories

Preliminary Class

365

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